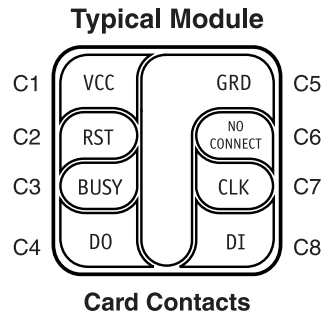


CLXSA004MF1

Smart Card

DESCRIPTION

The CLXSA004MF1 is a 2.7- 3.6 volt, serial EEPROM re-programmable memory smart card. It's 4,325,376 bits of memory are organized as 2048 pages of 264 bytes each. In addition to the main memory, the card also contains two SRAM data buffers of 264 bytes each. The buffers allow reception of data while a page in the main memory is being reprogrammed. Typical applications for the CLXSA004MF1 are data storage, digital voice storage and image storage. The smart card operates at clock frequencies up to 13 MHz with a typical active read current consumption of 4 mA. To allow for simple re-programmability, the CLXSA004MF1 does not require high input voltages for programming. The smart card operates from a single power supply, 2.7V to 3.6V, for both the program and read operations.



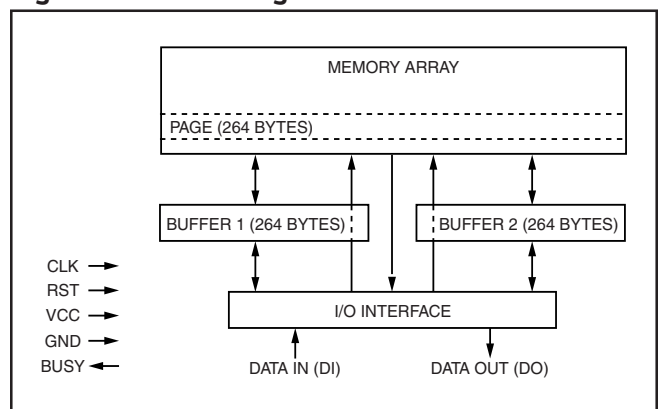
The CLXSA004MF1 is enabled through the chip select pin (CS) and accessed via a three-wire interface consisting of the Data Input (DI), Data Output (DO), and the Clock (CLK). All programming cycles are self-timed, and no separate erase cycle is required before programming.

The CLXSA004MF1 is available in the ISO 7810 standard, CR80, SIM/SAM and Keychain form factor.

FEATURES

- Single 2.7 to 3.6V Supply
- Page Program Operation
 - Single Cycle Reprogram (Erase and Program)
 - 2048 Pages (264 Bytes/Page) Main Memory
- Optional Page and Block Erase Options
- Two 264-byte SRAM Data Buffers - Allows receiving of data while reprogramming of non-volatile memory
- Continuous Read Capability through Entire Array
- Internal Program and Control Timer
- Low Power Dissipation
 - 4mA Active Read Current Typical
 - 2µA CMOS Standby Current Typical
- 13 MHz Max Clock Frequency
- Serial Peripheral Interface (SPI) Compatible - Modes 0 and 3
- CMOS and TTL Compatible Inputs and Outputs

Figure 1. Block Diagram



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