

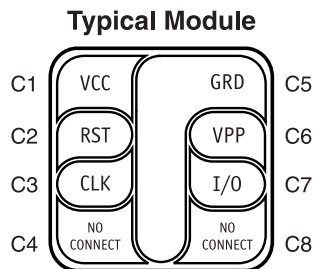
Technical Brief

CLXSU512KJ9MC-JV

JAVA Card™

DESCRIPTION

The CardLogix CLXSU512KJ9/MC-JV Smart Card features a powerful embedded 32 Bit microprocessor and the JAVA 2.1.1 Card Operating System. The card OS includes full support for the Global Platform 2.1 and GSM Phase 2 standards. The card fully complies with GSM 11.11 and 11.12 including low power idle and power down modes.



Card Contacts

Functionality includes issuer configurable advanced password security logic, DES, 3 DES and RSA Public Key Encryption, SHA 1 bi-directional authentication, and an advanced file management system. These cards have been developed for multiple use applications such as next generation cellular authentication/personalization and mobile network security combined with e-commerce and stored value systems.

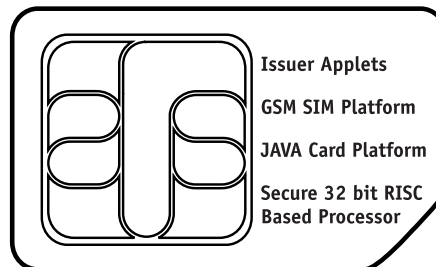
The CLXSU512KJ9/MC-JV provides the systems designer with the greatest degree of flexibility needed from microprocessor cards. The card features a 512k-Byte user memory (EEPROM), a hi-speed encryption co-processor, a hardware random number generator, and 256 bytes of RAM for fast key management. It also features full compliance to ISO 7816-1,2,3 and 4 with both T=0 and GSM protocols.

Program development is supported through third party visual tools for the JAVA Card environment. Card Communication is through 3GPP TS 03.19 SIM Toolkit Release 1999 as well as The Global Platform 2.1, Opencard and PC/SC APIs. A Crypto Service Provider "CSP" will be made available from CardLogix for this card. Additionally the card fully supports the PKCS #11 standard.

The card is available in the ISO 7810 standard, CR80, SIM/SAM form factor.

FEATURES

- Operating Voltage range: 3V to 5V
- Ultra low power CMOS (100µA standby, 10mA active)
- Each file can be read and write protected
- Triple layer programmable password security
- On Card key generation
- E-purse applet with anti-tearing is available
- RSA Signature Algorithm for certificate signing
- User memory is reconfigurable an unlimited number of times
- ATR Configuration and T=0, serial interface in accordance with ISO standard 7816 (asynchronous transmission), and GSM 11.11
- Data retention > 100 years
- Endurance: Minimum of 10⁵ write/erase cycles
- Electro-static discharge protection > 4,000V
- Optional high speed communications "PTS" negotiated mode up to 115k baud



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