

# CLXSU008KK5/T=0/c

## M.O.S.T.-lc Card™

### DESCRIPTION

The CardLogix CLXSU008KK5/T=0/c Smart Card features a powerful embedded 8 Bit microprocessor and the M.O.S.T.-lc card operating system that includes advanced password security logic, bi-directional authentication, and a simple file management system. This card has been developed for low cost single and multiple use applications such as retail-loyalty, network security, e-commerce and stored value.

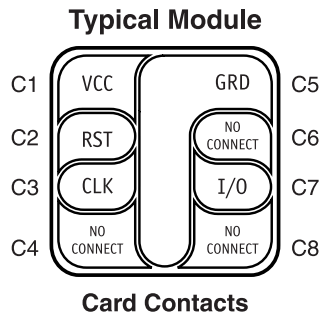
The CLXSU008KK5/T=0/c card provides the systems designer with the high degree of flexibility needed from microprocessor cards. The card features a 8K-bit/1K Bytes user memory (EEPROM) and a hardware random number generator. It complies with ISO 7816-1,2, and 3 with a T=0 protocol interface.

Program development is supported through the M.O.S.T. Toolz™, Winplex®, PC/SC API development software. Terminal libraries and a Crypto Service Provider “CSP” will be available from CardLogix for this card.

Authentication and CHV accesses to the card or specific files is set by the issuer. All cardholder verification files on the card can be created with specific access controls to prevent unauthorized reading or writing. Retry counters are used to track the number of incorrect password attempts. This mechanism prevents all further accesses to the files, as soon as the number of incorrect accesses exceeds the value in the retry register. A nonvolatile “kill bit” prevents further tampering.

This card is fully supported in the CardLogix M.O.S.T. Toolz™ development environment. The File Creation Utility enables the designer to completely configure the card using a GUI interface.

The card is available in the SIM/SAM, CR-80, and Keychain sizes with standard ISO 7816 pin outs.



### FEATURES

- Extensive anti-tampering design to guard against differential power and thermal attacks
- Operating Voltage range: 3V to 5V  
- Ultra low power CMOS (<100µA standby, <3mA active)
- Dynamic encryption for secure data transactions
- Each file can be read and write protected
- Double layer programmable password security
- User memory is reconfigurable an unlimited number of times, with protected programmable passwords
- ATR Configuration and T=0 serial interface in accordance with ISO standard 7816 (asynchronous transmission)
- Data retention > 100 years
- Endurance: Minimum of 10<sup>5</sup> write/erase cycles
- Electrostatic discharge protection > 4,000V

### PIN DEFINITIONS

Pin No	Card Contact	Symbol	Function
1	C1	VCC	Op. voltage 5V
2	C2	RST	Chip Control
3	C3	CLK	Clock
4	C4	No Connect	Not Connected
5	C5	GND	Ground
6	C6	No Connect	Not Connected
7	C7	I/O	Dataline
8	C8	No Connect	Not Connected



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